



## **LM Process Data AOI Guide, v2**

**11/26/2018**

This document covers the installation and use of an Add-On Instruction (AOI) for the Logix Designer software package from Rockwell Automation. This AOI handles cyclic IO-Link Process Data In and Process Data Out to and from a Banner LM sensor via an IO-Link Master connected to an Allen-Bradley PLC. The AOI covers parsing and display of the LM sensor Process Data In and Process Data Out. The AOI has one User Defined Tag data type.

### **Components**

Banner\_LM\_PD\_v2.L5X

### **UDT Packaged with the AOI**

Banner\_LM\_PDIO\_v2

### **Other AOIs Available Separately**

Banner has AOI files for controlling other Banner IO-Link devices and for a variety of IO-Link Masters. Banner also has AOI files for easily handling Banner device Process Data.

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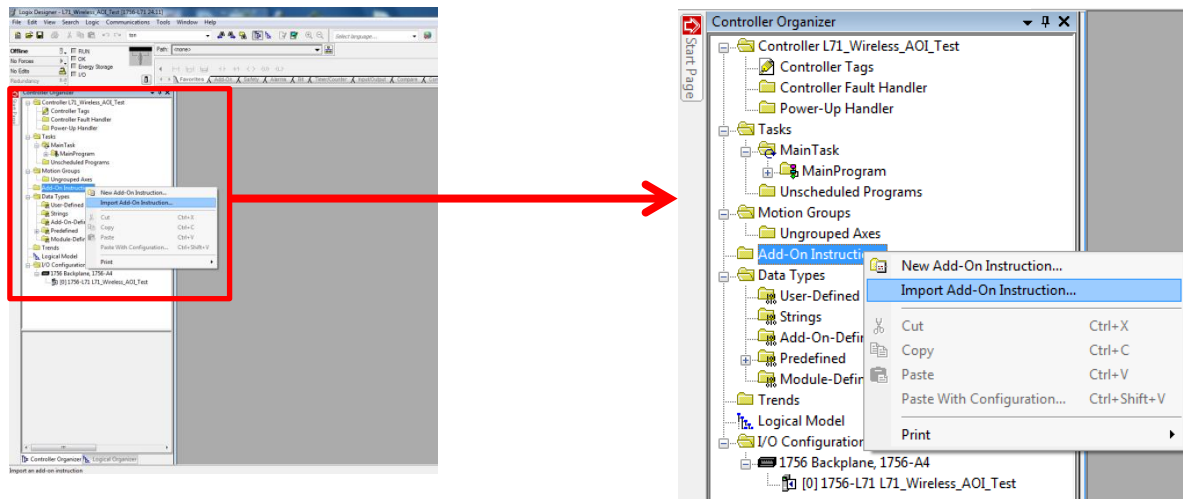
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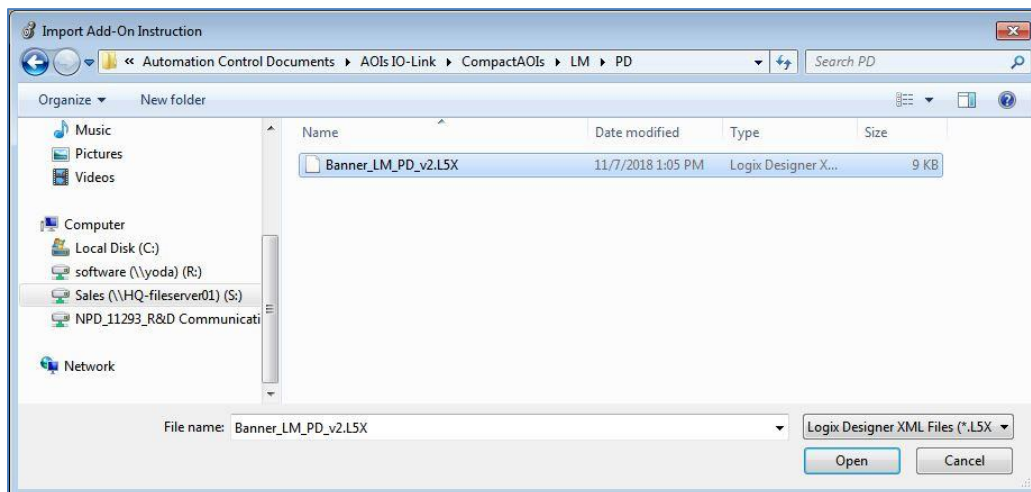
## 1. Installation Process

This section describes how to install the AOI in Logix Designer software.

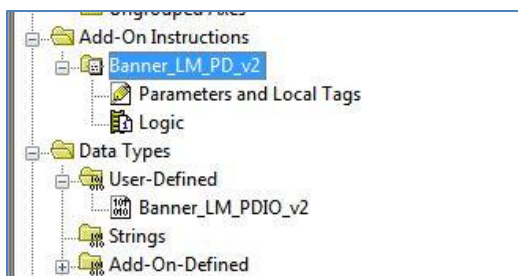
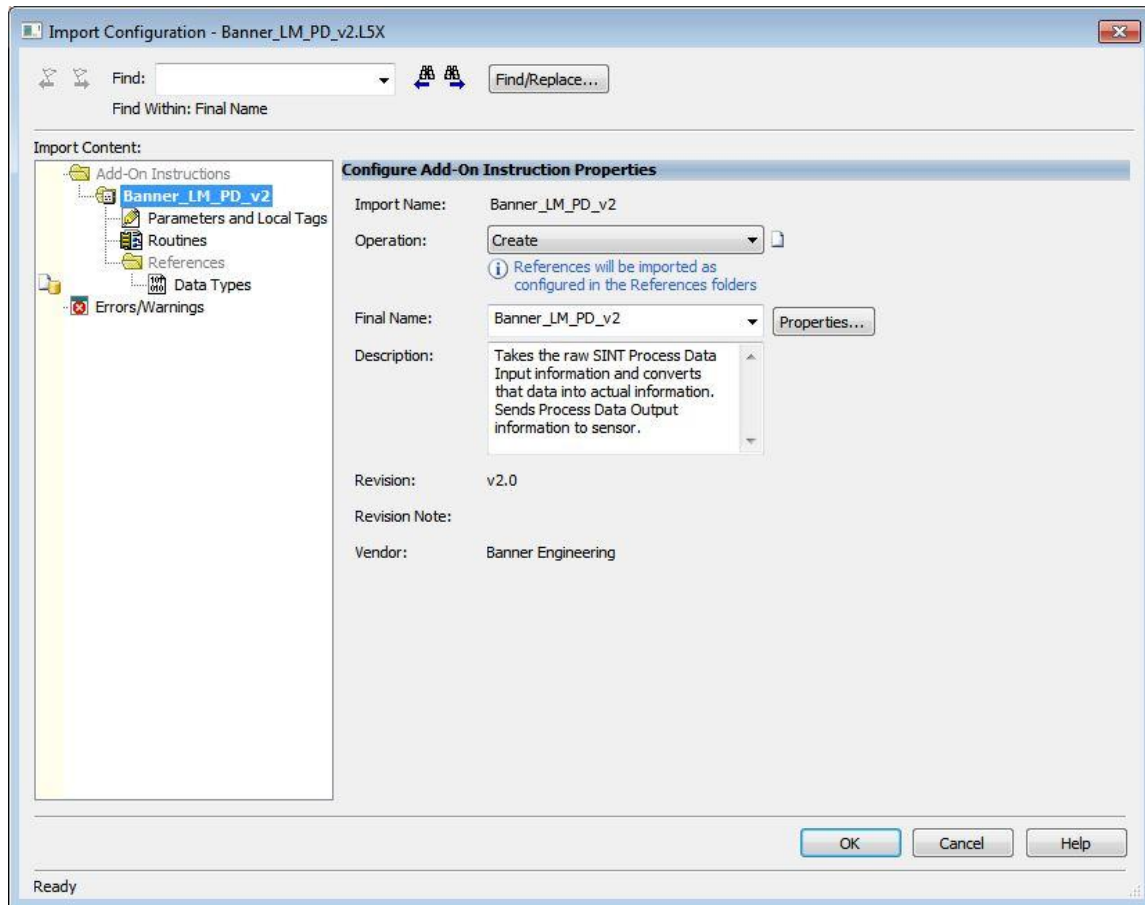
1. Open up a project.
2. In the Controller Organizer window, right-click on the Add-On Instruction folder. Select the Import Add-On Instruction option.



3. Navigate to the correct file location and select the AOI to be installed. In this example the "Banner\_LM\_PD\_v2.L5X" file will be selected. Click the Open button.



4. The Import Configuration window will pop up. The default selection will create all of the necessary items for the AOI. Click the OK button to complete the import process.



5. The AOI is added to the Controller Organizer window and should look similar to the picture at left.
6. AOI installation into the Logix Designer software complete.

## 2. Configuring the IO-Link Master

Make an EtherNet/IP connection to the IO-Link Master.

Create an Ethernet communications module for the IO-Link Master device. The controller tags generated include Input (I) and Output (O) Assembly Instances. Each Assembly has a corresponding tag array. Creating this Class 1 EtherNet/IP implicit IO connection will provide the PLC access to the IO-Link device Process Data. Each port on the IO-Link Master is given a dedicated group of I and O registers. See the relevant IO-Link Master User's Guide for more information.

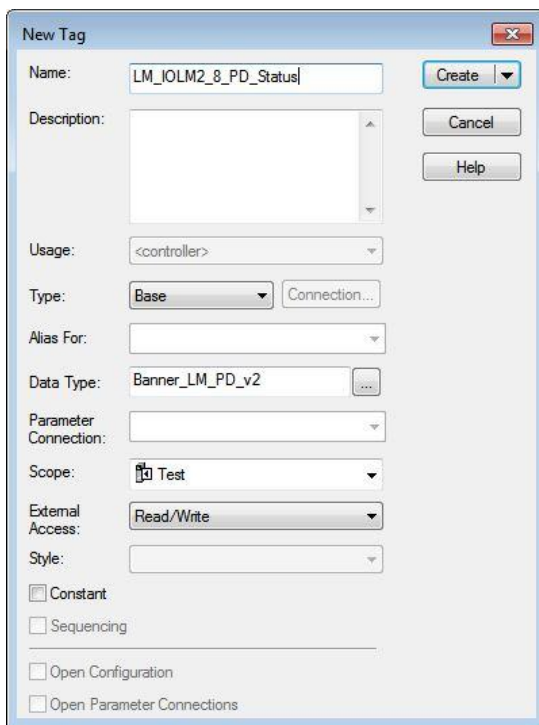
### 3. Configuring the AOI

1. Add the “Banner\_LM\_PD\_v2” AOI to your ladder logic program. For each of the question marks shown in the instruction we need to create and link a new tag array. The AOI includes a new type of User Defined Tags (UDT): a custom array of tags meant specifically for this AOI.



2. In the AOI, right-click on the question mark on the line labeled “Banner\_LM\_PD\_v2”. Click New Tag. Name the new tag. This example uses the name “LM\_IOLM2\_8\_PD\_Status”. The example naming convention accounts for this being an LM sensor connected to IO-Link Master #2, port #8, in our program. More masters could be named IOLM1, IOLM3, and different sensors could be connected at other port numbers, etc.

Note that the Data Type is the User-Defined Data Type (UDT) entitled “Banner\_LM\_PD\_v2”. This custom-made array of registers is specially built to handle the memory needs of this AOI. Click Create to make the tag array.



3. Now we will right-click on the question mark on the line labeled "Process\_Data" in the AOI. Click on "New Tag". Give the tag a name. This example uses the name "LM\_IOLM2\_8\_PD". Notice that the Data Type is "Banner\_LM\_PDIO\_v2". Click Create.

This array will handle the displaying of the parsed Process Data In and Process Data Out for the LM.

New Tag

Name: LM\_IOLM2\_8\_PD

Description:

Usage: <controller>

Type: Base Connection...

Alias For:

Data Type: Banner\_LM\_PDIO\_v2

Parameter Connection:

Scope: Test

External Access: Read/Write

Style:

☒ Constant

☐ Sequencing

☐ Open Configuration

☐ Open Parameter Connections

Create Cancel Help

4. The next three lines “Binary\_Include”, “PD\_Measurement\_1”, and “PD\_Measurement\_2”, allow the AOI to correctly interpret the Process Data In. In the case of the LM, there are two user-defined measurements in the Process Data, and there can be some extra bits included to tell the state of the discrete and analog outputs as well as the stability indicator. This AOI needs to know what choices have been made in the sensor for these three options.

There are two ways to achieve this goal. We can simply type in the correct number for each of these selections, or we can link this LM Process Data AOI to the LM Parameter Data AOI. See Appendix A for more information about LM Process Data In.

Takes the raw SINT Process Data Input information and converts that data into actual information. Sends Process Data Output information to sensor.

—Banner\_LM\_PD\_v2—

Takes the raw SINT Process Data Input information and converts that data into actual information. Sends Process Data Output information to sensor.

—Banner\_LM\_PD\_v2—

Field	Value
Binary_Include	1
PD_Measurement_1	1
PD_Measurement_2	0
Byte_Swap	?

Field	Value
Binary_Include	LM_IOLM2_8.Write_Data.Include_Binary_in_PDI
PD_Measurement_1	LM_IOLM2_8.Write_Data.Process_Data_Measurement_1_Selection
PD_Measurement_2	LM_IOLM2_8.Write_Data.Process_Data_Measurement_2_Selection
Byte_Swap	?

NOTE: if you type in the incorrect number (i.e. it does not match the sensor’s current configuration) you will get incorrectly displayed Process Data In information.

**Binary Include:** the options here are “0” (don’t include output 1 state, analog output state, and stability indicator state in Process Data In) and “1” (do included those bits). The default is “1”.

**PD\_Measurement\_1:** the options here are “0” (Disabled), “1” (Distance Measurement Value), “2” (Displayed Distance Measurement Value), “3” (Analog Output Value), and “4” (BDC1 Dual Mode Percent). The default is “1”.

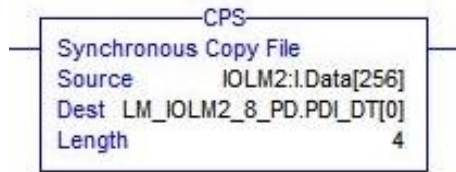
**PD\_Measurement\_2:** the options here are “0” (Disabled), “1” (Excess Gain), “2” (Excess Gain/10), and “3” (BDC1 Dual Mode Percent). The default is “0”.

5. The last line in the AOI is a setting to account for byte swapping. In the case of the LM, the Process Data is four bytes long. IO-Link Masters may read each pair of bytes in either order, so this AOI has to be ready to perform a byte swap. Enter a “0” or a “1” to toggle this setting. See Appendix B for more information.

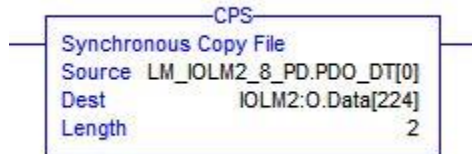


6. The final two steps required before we download and run the LM Process Data AOI involve a pair of File Synchronous Copy (CPS) instructions. These instructions allow the AOI to read from and write to the raw Process Data values found in the register tags of the IO-Link Master.

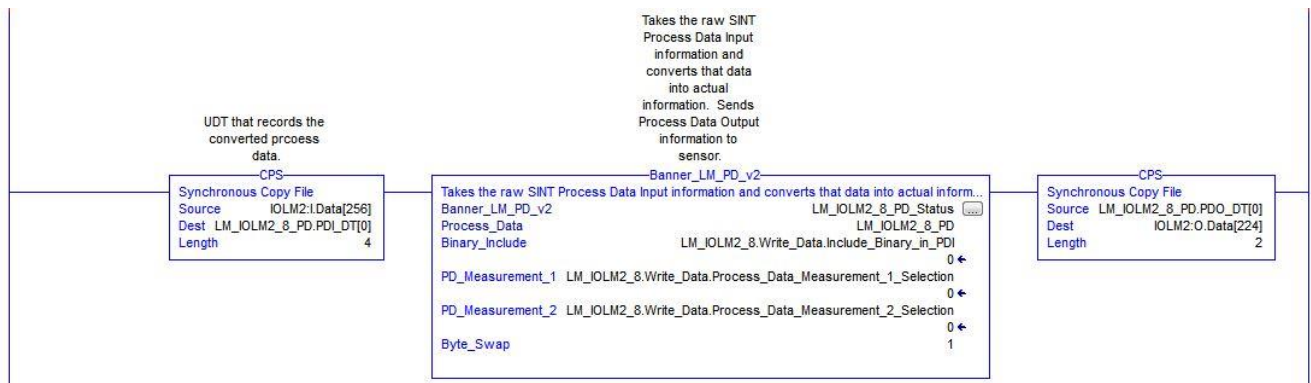
Add a CPS instruction before the AOI on the ladder rung that looks like the one seen below. Refer to Appendix B for which byte to start with in the “Source” area. In this case, the IO-Link Master in question has the raw Process Data In values for a device connected to port 8 starting at byte 256. For the “Destination”, we will enter the “PDI\_DT[0]” location, as seen below. Finally, the length will be 4 bytes, as that is the size of the LM Process Data In.



Another CPS instruction is added to the AOI rung, this time after the AOI. This CPS instruction is used to copy Process Data Out from the AOI into the raw Process Data Out registers used by the IO-Link Master. See Appendix B for more information. In this example, we will connect the AOI's "PDO\_DT[0]" to the starting byte location for port 8 in the Process Data Out side. In this example, that is byte 224. The size to be copied is 2 bytes.

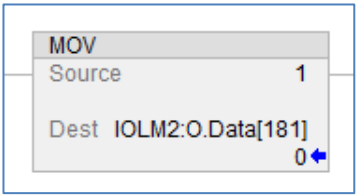


Here is what the entire rung looks like when completed.



If a Banner IO-Link Master is being used, setup a Move block. Send a 1 to the Activate Outputs array value (see table for each port’s value). As an example, if port 1 needs the process data outputs active then send a 1 to 181.

IO-Link Master Port	Activate Outputs
1	181
2	215
3	249
4	283
5	317
6	351
7	385
8	419



The “Banner\_LM\_PD\_v2” AOI is now ready for use.

## 4. Using the AOI

The “Banner\_LM\_PD\_v2” Add-On Instruction has created a group of tags representing the LM Process Data, broken out into its component parts.

Look in the Controller Tags to find the name you used above. This example used the name “LM\_IOLM2\_8\_PD”. The tag array, seen below, has individual pieces of information instead of 32 unlabeled bits. The first three items are the on/off status of the discrete output channel, the state of the analog output, and the stability indicator. The fourth and fifth items are the Measurement 1 and 2 values.

The next item is the Process Data Out for the LM. Labeled “Transducer Disable”, this variable controls the LM laser transducer. A “0” will keep the laser on, while a “1” disables the laser.

[-] LM_IOLM2_8_PD	{...}	{...}		Banner_LM_PDIO_v2
[+] LM_IOLM2_8_PD.Channel_1_Output_State	1		Decimal	SINT
[+] LM_IOLM2_8_PD.Analog_State	1		Decimal	SINT
[+] LM_IOLM2_8_PD.Sensor_Stability	1		Decimal	SINT
[-] LM_IOLM2_8_PD.Measurement_Value_1	132.639		Float	REAL
[-] LM_IOLM2_8_PD.Measurement_Value_2	108.0		Float	REAL
[+] LM_IOLM2_8_PD.Transducer_Disable	0		Decimal	INT
[+] LM_IOLM2_8_PD.PDI_DT	{...}	{...}	Decimal	SINT[4]
[+] LM_IOLM2_8_PD.PDO_DT	{...}	{...}	Decimal	SINT[2]

## Appendix A LM Process Data

The LM has 4 bytes of Process Data In. There are two modes for displaying this data, as shown below. The first is mode 1.

ProcessDataIn "Process Data Input" id=PD_ProcessDataIn									
bit length: 32 data type: 32-bit Record (subindex access not supported)									
subindex	bit offset	data type	allowed values	default value	acc. restr.	mod. other var.	excl. from DS	name	description
1	0	Boolean	false = Inactive, true = Active					Channel 1 Output State	Channel 1 Output State
2	1	Boolean	false = Inactive, true = Active					Analog State	Determine whether the measured process data distance is within the analog window.
3	2	Boolean	false = No target or Marginal, true = Stable					Stability	Stability state
4	3	19-bit Integer						Measurement 1 Value	The selected process data measurement 1 value.
5	22	10-bit UInteger						Measurement 2 Value	The selected process data measurement 2 value.

This Process Data is mapped to a specific group of EtherNet/IP registers. The 32-bits of Process Data actually encode five separate pieces of information. Bit 0 is the state of BDC1 (Binary Data Channel 1, also known simply as Output Channel 1). Bit 1 is the analog state indicator and bit 2 is the stability indicator. The remaining 29 bits are used to communicate the LM Measurement 1 and Measurement 2 values.

This AOI intelligently parses this Process Data into its component pieces.

In mode 0 only the measurement value is provided with no binary status bits (no Output 1, Analog, or Stability).

ProcessDataIn "Process Data Input" id=PD_ProcessDataInWithoutBinary									
bit length: 32 data type: 32-bit Record (subindex access not supported)									
subindex	bit offset	data type	allowed values	default value	acc. restr.	mod. other var.	excl. from DS	name	description
1	0	19-bit UInteger						Measurement 1 Value	The selected process data measurement 1 value.
2	19	13-bit UInteger						Measurement 2 Value	The selected process data measurement 2 value.

The LM has 1 byte of Process Data Out. The value controls whether the laser is disabled.

ProcessDataOut "Process Data Output" id=PD_ProcessDataOut										
bit length: 8 data type: 8-bit Record (subindex access not supported)										
subindex	bit offset	data type	allowed values	default value	acc. restr.	mod. other var.	excl. from DS	name	description	
1	0	Boolean	false = Active, true = Inactive					Transducer Disable		

## Appendix B IO-Link Master Cheat Sheet

Different IO-Link Masters behave differently in several ways. For one, the register locations where Process Data is stored varies. For another, some IO-Link Masters require byte-swapping and/or word-swapping. The tables below aim to define some of these differences. Note that these numbers are when using all default settings. IO-Link Masters can change the register locations to which Process Data is mapped in response to non-default, optional settings. See relevant IO-Link Master documentation for more information.

PDI (Process Data In) is found in the IO-Link Master's T->O (PLC "Input") Assembly Instance.

PDO (Process Data Out) is found in the IO-Link Master's O->T (PLC "Output") Assembly Instance.

**Table 1. First Register of Process Data "SINT0"**

Port	Allen-Bradley*		Control		Balluff		Turck		ifm		Banner	
	PDI	PDO	PDI	PDO	PDI	PDO	PDI	PDO	PDI	PDO	PDI	PDO
1	I.Ch0Data[0]	O.Ch0Data[0]	4	0	8	6	6	4	190	46	184	182
2	I.Ch1Data[0]	O.Ch1Data[0]	40	32	56	38	38	36	222	78	218	216
3	I.Ch2Data[0]	O.Ch2Data[0]	76	64	104	70	70	68	254	110	252	250
4	I.Ch3Data[0]	O.Ch3Data[0]	112	96	152	102	102	100	286	142	286	284
5	I.Ch4Data[0]	O.Ch4Data[0]	148	128	200	134	134	132	318	174	320	318
6	I.Ch5Data[0]	O.Ch5Data[0]	184	160	248	166	166	164	350	206	354	352
7	I.Ch6Data[0]	O.Ch6Data[0]	220	192	296	198	198	196	382	238	388	386
8	I.Ch7Data[0]	O.Ch7Data[0]	256	224	344	230	230	228	414	270	422	420

\*see relevant Banner Allen-Bradley IO-Link Master AOI Guide and Allen-Bradley User Guides for more information on using device IODD files to aid in integration.

Note: Murr IO-Link Masters have configurable process data. Refer to the Murr IO-Link Master Instruction Manual for Process Data mappings.

**Table 2. Byte-Swap**

IO-Link Master	Byte Swap
Allen-Bradley	0
Control	1
Balluff	0
Turck	1
ifm	1
Murr	0
Banner	0

Specific hardware used in both tables (all default settings):

- Allen-Bradley Armor Block I/O IO-Link Master (1732E-8IOLM12R)
- Control 8-EIP IO-Link Master (99608-8)
- Balluff BNI006A (BNI EIP-508-105-Z015)
- Turck TBEN-L5-8IOL
- ifm AL1122
- Murr Impact67 E DIO 12 DIO4/IOL4 4P (Art.-No. 55144)

Banner IO-Link Masters (DXMR90-4K) have a port status register. The register gives the status of the port. It gives information on if the port has an IO-Link device connected and if Process Data is valid. This is optional information but is useful for troubleshooting. The data comes into the PLC as bytes while the literature shows the value as a word. The table below gives the upper and lower byte data location in the PLC. The upper byte includes bits 15 through 8, while the lower byte has bits 7 through 0.

IO-Link Master Port	Upper Bits 15 - 8	Lower Bits 7 - 0
1	182	183
2	216	217
3	250	251
4	284	285
5	318	319
6	352	353
7	386	387
8	420	421

#### Port Status:

**Bit0** = Connected?  
**Bit1** = Process Data Valid?  
**Bit2** = Event Pending?  
**Bit3** = Ready for ISDU?  
**Bit4** = Pin4 SIO State  
**Bit5** = Pin2 SIO State

#### Bit6-7 = Pin4 Mode:

SDCI Mode = 0  
 SIO Input Mode = 1  
 SIO Output Mode = 2

#### Bit8-10 = Pin2 Mode:

Disabled = 0  
 Input Normal = 1  
 Output = 2  
 Diagnostic Input = 3  
 Inverted Input = 4